



YEREVAN, NOR NORK 7th DIST., KARAKHANYAN 33, H.8



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SERGO@SYNOPSYS.COM

PERSONAL INFORMATION

Birth date

- 29.02.2000
- Citizenship
- Republic of Armenia

Passport

AP 0427415
 Family status
 SINGLE

HARUTYUNYAN SERGO

WORK EXPERIENCE

SYNOPSYS ARMENIA EDUCATIONAL DEPARTMENT (SAED) / INTERN IN SWG 08.01.2019 – 01.08.2019

Responsibilities

 Schematic and layout design and verification of SRAM

SYNOPSYS ARMENIA INC./ ANALOG AND MIXED SIGNAL IC DESIGN ENGINEER I 01.08.2019 – 01.11.2021

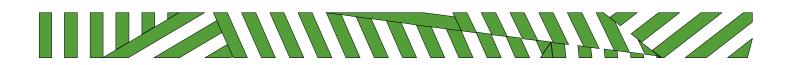
SYNOPSYS ARMENIA INC./ ANALOG AND MIXED SIGNAL IC DESIGN ENGINEER II 01.11.2021 – till now

Responsibilities

Schematic design and verification parts of USB, HDMI and AUX protocols

POLYTECH HIGH SCHOOL 01.09.2021 – till now

- Responsibilities
- Teach microelectronics





SKILLS

Languages

- Armenian-native
 - English-good
 - Russian-good

Programing languages

- C ++ (Basic)
- Computer skills
- Synopsys Tools

Computer skills

- MS Office
- Internet
- Other skills
- Circuit Design and Analysis
 - Algorithmic thinking

INTERESTS

- IC Design
- Electronics
- Programing
- Mathematics
- Nanotechnologies
- Innovative technologies
 - Robotics

EDUCATION

UNIVERSITY / 2016 - 2020

National polytechnic university of Armenia (bachelor)

Faculty

 Synopsys Armenia Educational Department (2018 - 2020)
 Profession
 IC Design

UNIVERSITY / 2020 – 2022 National polytechnic university of Armenia (master)

Faculty

Synopsys Armenia Educational Department (2020 - 2022)

Profession

IC Design

VOLUNTEER EXPERIENCE OR LEADERSHIP

VOLUNTEER / «AGN FOREVER PARTNERS» LTD 01.06.2015 - 01.09.2015

Activity

- Charge printer cartridges
- Remake printers and cartridges
 - Remake computers
 - Explore computer programs





ADDITIONAL INFORMATION

PUBLISHED ARTICLES

- Two Stage CTLE For High Speed Data Receiving, Proceedings of IEEE 40th International Conference on Electronics and Nanotechnology (ELNANO), Kyiv, Ukraine, 2020
- A Low Dropout Voltage Regulator with Higher than Supply Output Voltage and Load Based Frequency Control System, Proceedings of IEEE 40th International Conference on Electronics and Nanotechnology (ELNANO), Kyiv, Ukraine, 2020
- IO Glitch Filter, Proceedings of IEEE 40th International Conference on Electronics and Nanotechnology (ELNANO), Kyiv, Ukraine, 2020
- Reliable PMOS Based Charge Pump Architecture, Proceedings of NAS RA and NPUA, Series of Technical Sciences, Yerevan, Armenia, 2020

PARTICIPATION IN CONFERENCES

 2020 IEEE 40th International Conference on Electronics and Nanotechnology (ELNANO)

